EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	0	((driving adj circuit) with (plasma adj display adj panel) and (sustain adj voltage adj applying adj circuit) with (sustain$3 adj pulse adj generating adj circuit) with (generating adj sustain adj pulse) with (predetermin$3 adj waveform) and (offset adj pulse adj generating adj circuit) near (generating adj offset adj pulse) with (higher adj peak adj value) near (sustain adj pulse) and (sustain adj pulse adj circuit) and (offset adj pulse adj generating adj circuit) with (connect$3 adj parallel) and (offset adj pulse adj generating adj circuit) with (source) near (first adj voltage) with (applying adj offset adj voltage) and (first adj switching adj circuit) with (first adj voltage) with (display adj electrodes) and (inductance adj component) with (resonance adj voltage) with (applying adj offset adj voltage) and (forward adj diode) with (current adj suppl$3) with (display adj electrodes) with (flow adj forward) same (resonance adj voltage) with (maintain$3 adj higher adj voltage adj level) near ("than") same (sustain adj voltage adj level) with (predetermin$3 adj period adj time) and (sustain adj pulse adj generating adj circuit) with (source near second adj voltage) with (applying adj sustain adj voltage) and (second adj switching adj circuit) with (applying near second adj voltage) near (display adj electrodes)).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/06/09 03:26